

AMENDMENTS TO THE SPECIFICATION

Please amend page 21, lines 10-14 as follows:

By way of example, any of the multiplexers (selection circuits) discussed herein, such as for example multiplexers 86, 88, 90, 92, 94, 96, 98, 100, 102, 104, may comprise any of the selection circuits described in ~~co~~~~pend~~~~ing~~ U.S. Patent Application No. 09/250,781, filed February 12, 1999, ~~entitled~~ ~~“MULTI DIMENSIONAL PROGRAMMABLE INPUT SELECTION APPARATUS AND METHOD”~~now abandoned, ~~invented by Chung yuan Sun,~~ and commonly assigned herewith, the full disclosure of which is hereby incorporated into the present application by reference.

Please amend page 30, lines 19-25 through page 31, lines 1-7 as follows:

The full disclosures of the following ~~co~~~~pend~~~~ing~~ U.S. patent applications are hereby incorporated into the present application by reference: U.S. Patent Application No. 09/231,998, filed January 15, 1999, ~~entitled~~ ~~“STORAGE CELLS UTILIZING REDUCED PASS GATE VOLTAGES FOR READ AND WRITE OPERATIONS”~~now abandoned, and commonly assigned herewith; U.S. Patent Application No. 09/281,008, filed March 30, 1999, ~~entitled~~ ~~“METHOD AND APPARATUS FOR INCREASING THE SPEED OF FIELD PROGRAMMABLE GATE ARRAYS”~~now issued as United States Patent Number 6,301,696, and commonly assigned herewith; U.S. Patent Application No. 09/285,563, filed April 2, 1999, ~~entitled~~ ~~“METHOD AND APPARATUS FOR STORING A VALIDATION NUMBER IN A FIELD~~

~~PROGRAMMABLE GATE ARRAY~~”now issued as United States Patent Number 6,446,242, and commonly assigned herewith; and U.S. Patent Application No. 09/318,198, filed May 25, 1999, ~~entitled “INTEGRATED CIRCUIT THAT INCLUDES A FIELD PROGRAMMABLE GATE ARRAY AND A HARD GATE ARRAY HAVING THE SAME UNDERLYING STRUCTURE”~~now issued as United States Patent Number 6,221,697, and commonly assigned herewith.